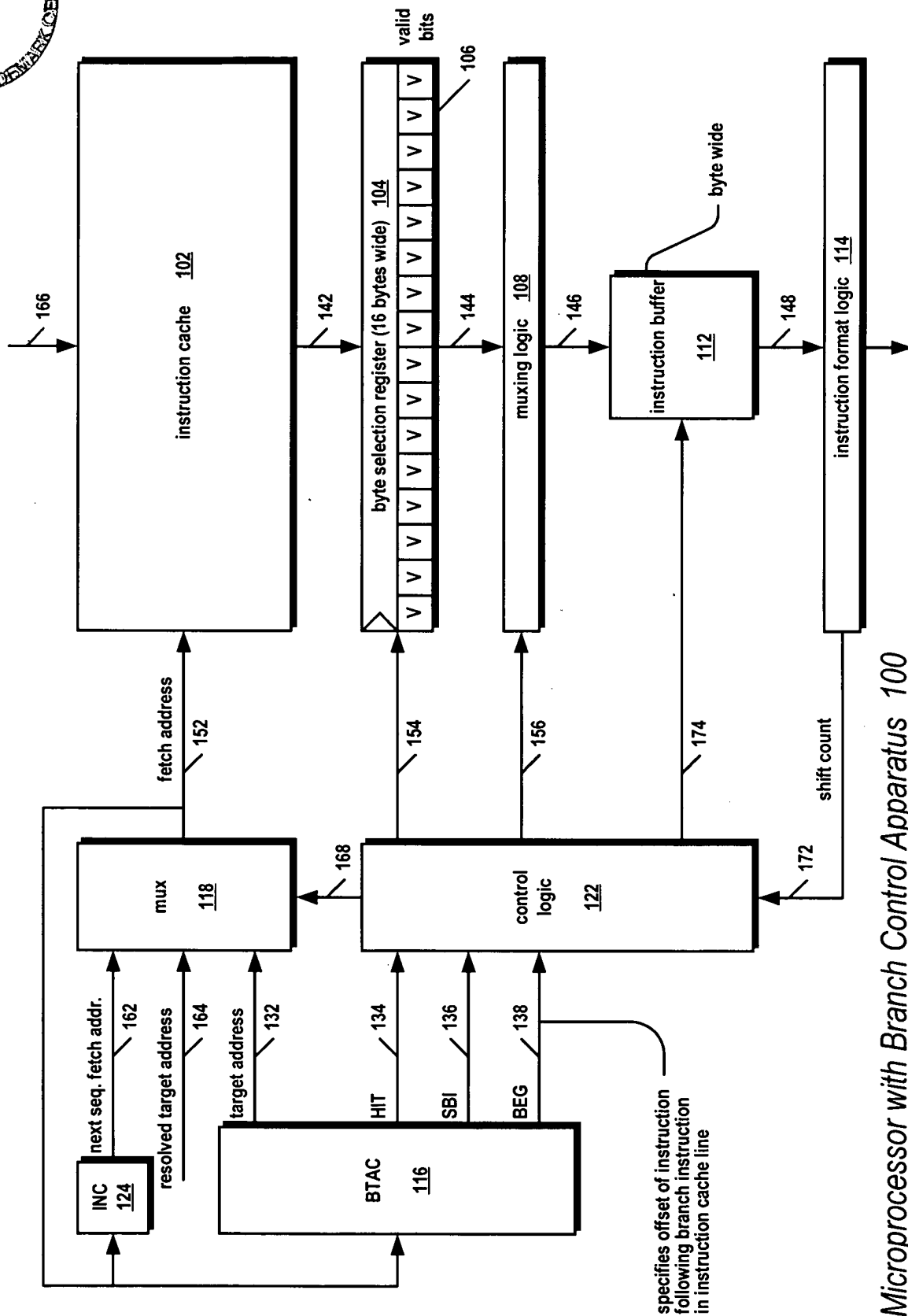


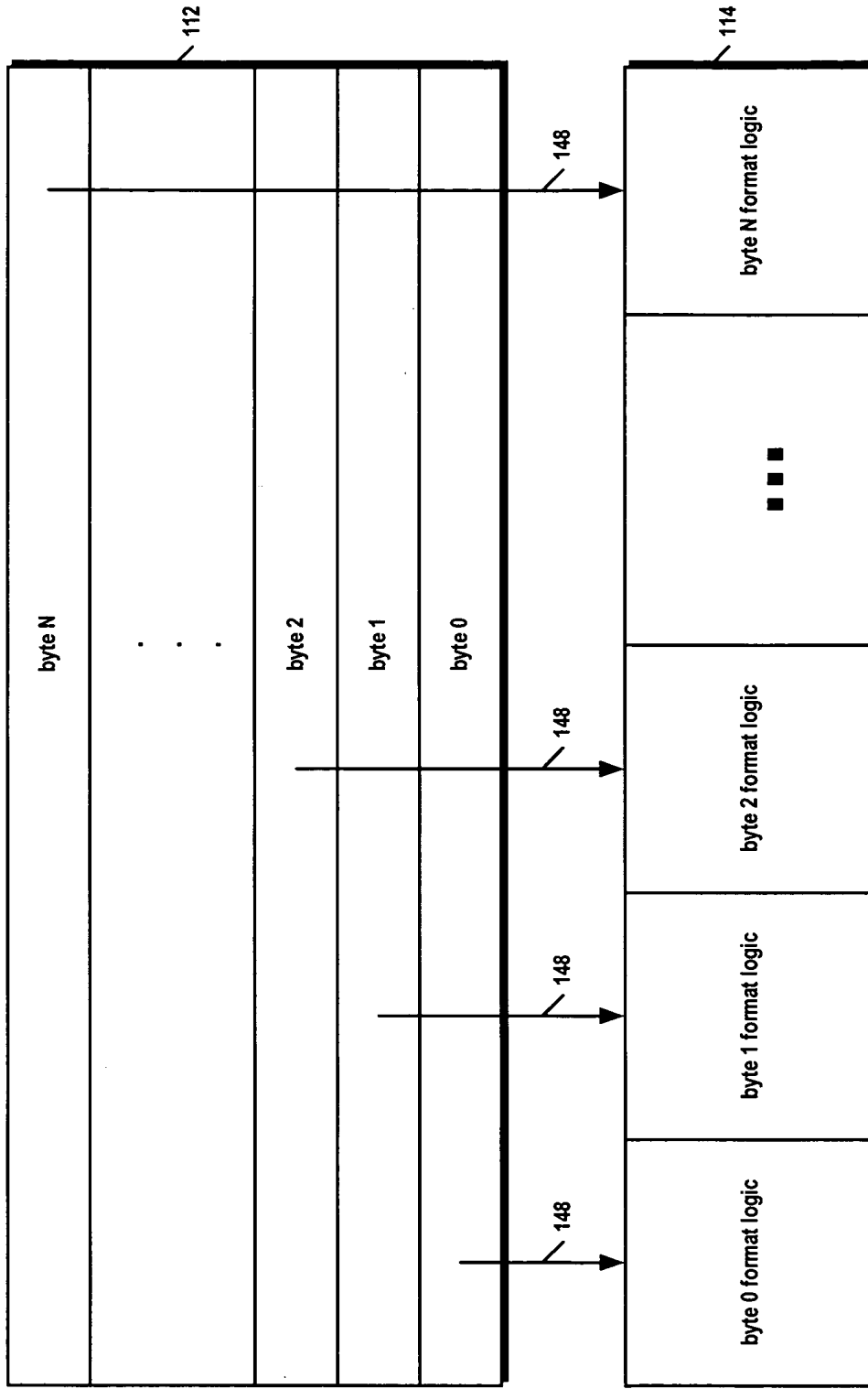
Replacement Sheet

FIG. 1



Microprocessor with Branch Control Apparatus 100

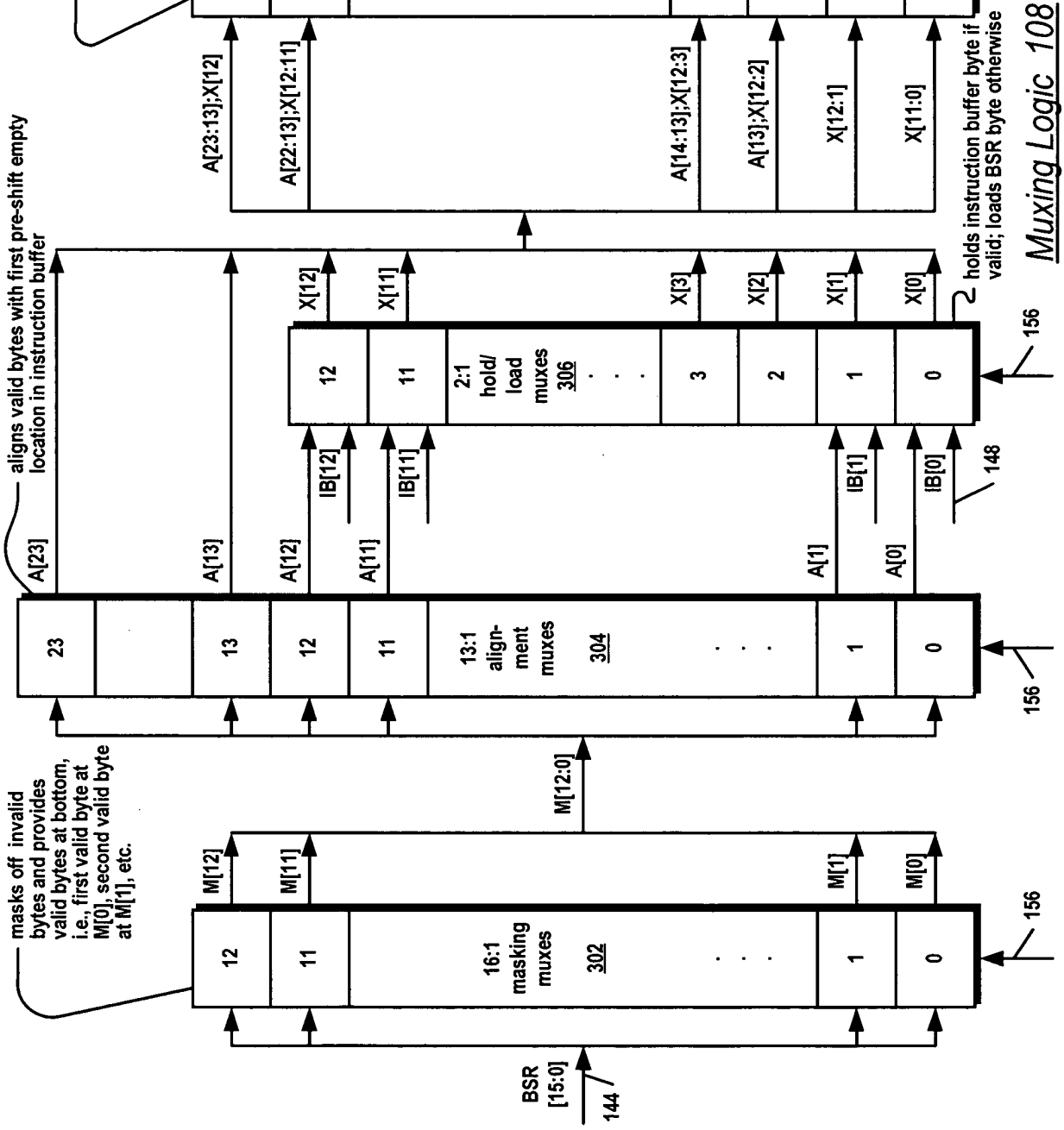
FIG. 2



Instruction Buffer to Instruction Format Logic Coupling 100

FIG. 3

shifts selected bytes down by number of bytes to be shifted out of instruction buffer, i.e., number of bytes already formatted by format logic



Muxing Logic 108

FIG. 4

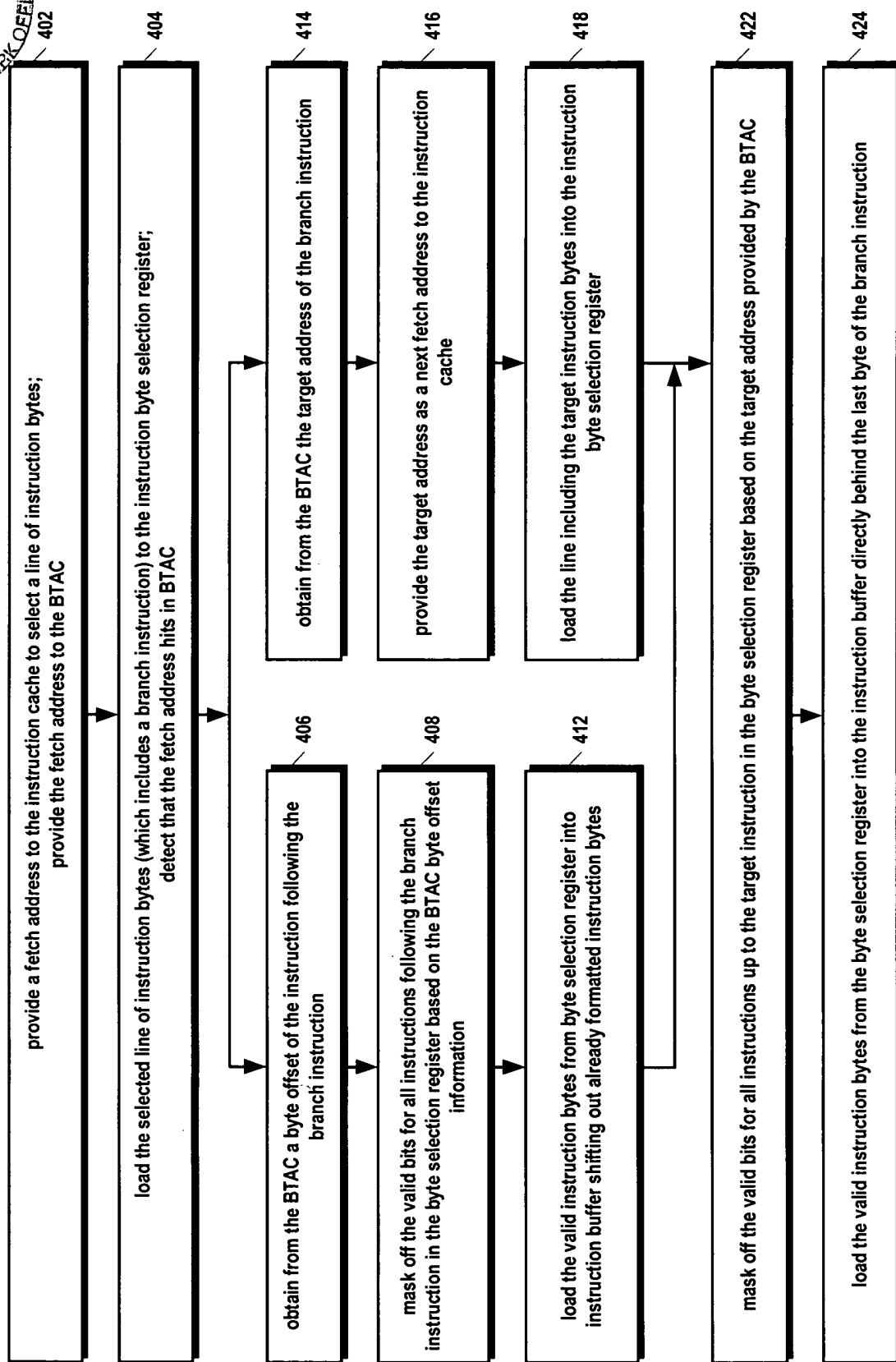
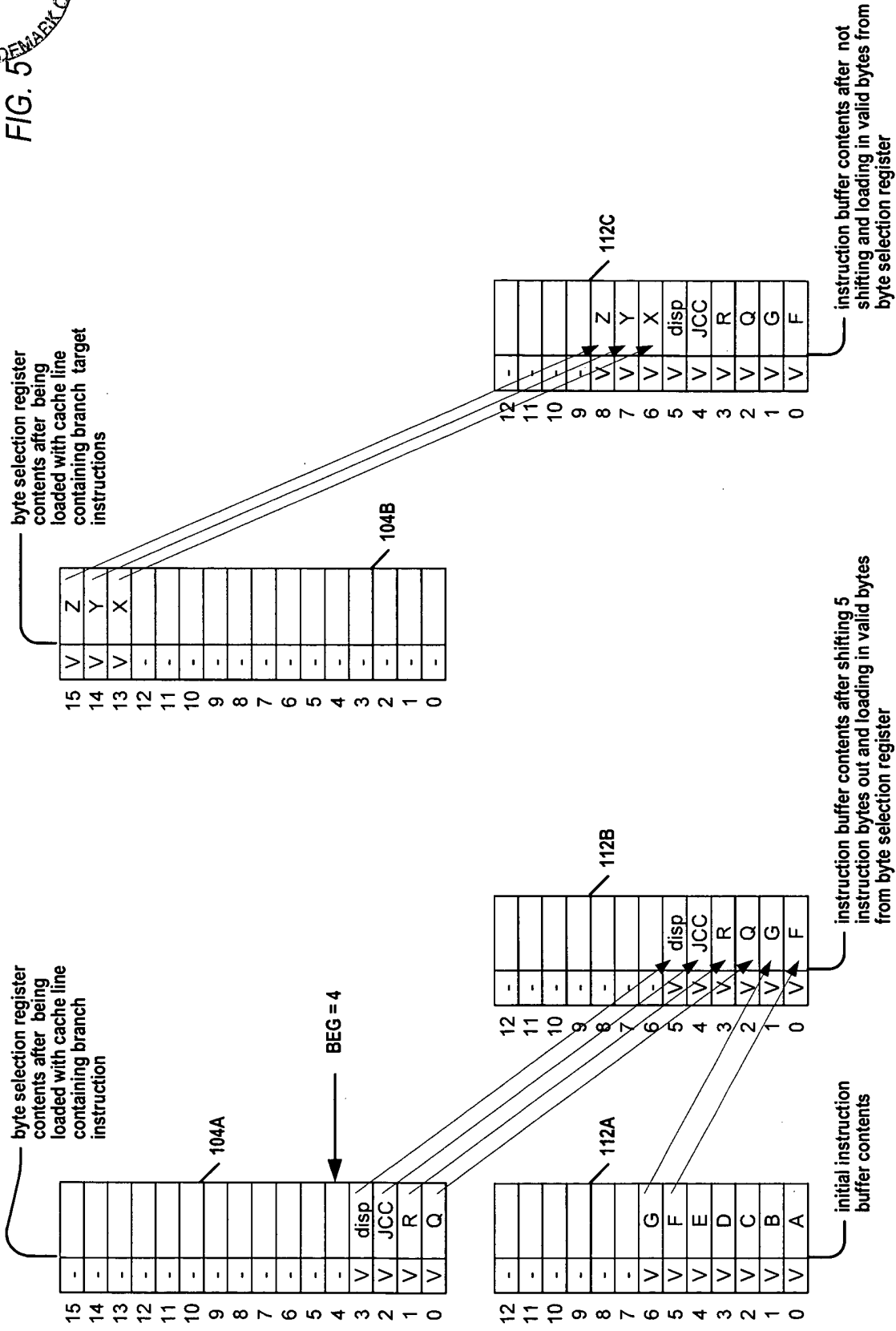
Instruction Byte Selection Method

FIG. 5

Replacement Sheet



Branch Control Apparatus Operation Example